		Low Low
	Application No.	Applicant(s)
Notice of Allowability	10/080,121	WAKISAKA ET AL.
	Examiner	Art Unit
	Quyen P. Leung	2828
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT THE Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in this a or other appropriate communication GHTS. This application is subject	pplication. If not included on will be mailed in due course. THIS
1. This communication is responsive to <u>2/13/2004</u> .		
2. The allowed claim(s) is/are <u>1-90</u> .		
3. $\boxtimes$ The drawings filed on <u>22 February 2002</u> are accepted by th	e Examiner.	
<ul> <li>4.</li></ul>	been received. been received in Application No. cuments have been received in this cuments have been received in this of this communication to file a replication of this application.  Itted. Note the attached EXAMINE is reason(s) why the oath or declar to be submitted. It is not be submitted. It is application on the drawner header according to 37 CFR 1.12 is it of BIOLOGICAL MATERIAL	s national stage application from the  y complying with the requirements  R'S AMENDMENT or NOTICE OF ration is deficient.  D-948) attached  Office action of vings in the front (not the back) of 1(d).  must be submitted. Note the
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date</li></ul>	6. ☐ Interview Summan Paper No./Mail D 8), 7. ☐ Examiner's Amen	ate

## **REASONS FOR ALLOWANCE**

1. The following is an examiner's statement of reasons for allowance: Claims 1-90 are allowed because the cited prior art do not teach or fairly suggest a semiconductor laser module comprising a thermistor and the laser diode joined to a carrier through a multilayer film including a gold film so that the temperature of the semiconductor laser element is accurately measured and controlled.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quyen P. Leung whose telephone number is (571)272-1943. The examiner can normally be reached on 9-5:30, M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571)272-1834. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2828

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Quyen P. Leung Primary Examiner Art Unit 2828

**QPL**